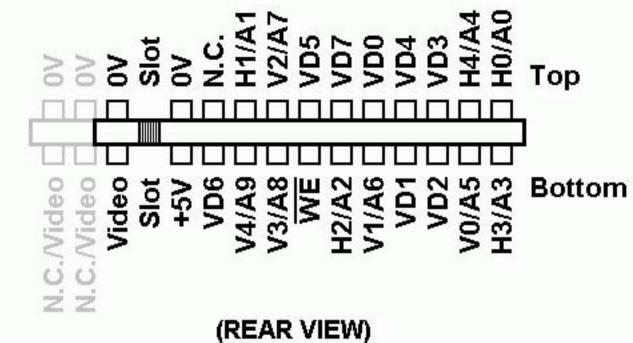


### **Jupiter Ace Video Connector**



#### (IXEAIX VIEVV)

Plan de cablage A = dessus B = dessous							
ACE	ZX	Signal		ACE	ZX	Signal	
1A	-	-		1B		_	
2A		-		2B	11A	INT	
ЗА	7A	D6		3B	12A	NMI	
4A	5A	D1		4B	13A	HALT	
5A	17B	A0		5B	14A	MREQ	
6A	5B	A7		6B	15A	IORQ	
7A	6B	A8		7B	16A	RD	
8A	7B	A9		8B	17A	WR	
9A	8B	A10		9B	18A	BUSAK	
10A	15B	A2			19A	WAIT	
11A	8A	D5	1	11B	20A	BUSRQ	
12A	9A	D3	1	12B	21A	RESET	
13A	10A	D4		13B	22A	M1	
14A	13B	A15		14B	23A	RFSH	
15A	12B	A14	1	15B	4B	A6	
16A	11B	A13		16B	3B	A5	
17A	10B	A12	1	17B	2B	A4	
18A	9B	A11	1	18B	14B	A3	
19A	18B	Horloge	1	19B	16B	A1	
20A	23B	5V			4A	D0	
21A	22B	9V		21B	6A	D2	
22A	_	_		22B	1A	D7	
23A	19B+20B	0V			_	_	
	ACE 1A 2A 3A 4A 5A 6A 7A 8A 9A 10A 11A 12A 13A 14A 15A 16A 17A 18A 19A 20A 21A 22A	ACE ZX  1A - 2A - 3A 7A 4A 5A 5A 17B 6A 5B 7A 6B 8A 7B 9A 8B 10A 15B 11A 8A 12A 9A 13A 10A 14A 13B 15A 12B 16A 11B 17A 10B 18A 9B 19A 18B 20A 23B 21A 22B 22A -	ACE ZX Signal  1A	ACE ZX Signal  1A	ACE         ZX         Signal         ACE           1A         -         -         1B           2A         -         -         2B           3A         7A         D6         3B           4A         5A         D1         4B           5A         17B         A0         5B           6A         5B         A7         6B           6A         5B         A7         6B           7A         6B         A8         7B           8A         7B         A9         8B           9A         8B         A10         9B           10A         15B         A2         10B           11A         8A         D5         11B           12A         9A         D3         12B           13A         10A         D4         13B           14A         13B         A15         14B           15A         12B         A14         15B           16A         11B         A13         16B           17A         10B         A12         17B           18A         9B         A11         18B           19A	ACE         ZX         Signal         ACE         ZX           1A         -         -         1B         -           2A         -         -         2B         11A           3A         7A         D6         3B         12A           4A         5A         D1         4B         13A           5A         17B         A0         5B         14A           6A         5B         A7         6B         15A           7A         6B         A8         7B         16A           8A         7B         16A         8B         17A           9A         8B         A10         9B         18A           10A         15B         A2         10B         19A           11A         8A         D5         11B         20A           12A         9A         D3         12B         21A           13A         10A         D4         13B         22A           14A         13B         A15         14B         23A           15A         12B         A14         15B         4B           16A         11B         A13         16B	ACE         ZX         Signal         ACE         ZX         Signal           1A         —         —         —         —         —           2A         —         —         —         —         —           3A         7A         D6         3B         12A         NMI           4A         5A         D1         4B         13A         HALT           5A         17B         A0         5B         14A         MREQ           6A         5B         A7         6B         15A         IORQ           7A         6B         A8         7B         16A         RD           8A         7B         AB         RD         WR           9A         BB         A10         BB         18A         BUSAK           10A         15B         A2         10B         19A         WAIT           11A         8A         D5         11B         20A         BUSRQ           12A         9A         D3         12B         21A         RESET           13A         10A         D4         13B         22A         M1           14A         13B         A15

## INNOVONICS TRUMPCARD - INSTRUCTIONS

Trumpcard was originally designed as a mechanical and electrical link between the Jupiter Ace and ZX peripherals. To maintain flexibility and programmability the two connecting edges are quite separate. In this way the constructor can colour code and interrupt the signal lines as he pleases, both for experimental and permanent applications.

With the exception of the chip select lines, WE on the ACE and RAM/ROM C.S.on the ZX 81, both computers share the same bus albeit in a different configuration.

# STANDARD CONSTRUCTION - GENERAL PURPOSE, ALL LINES CONNECTED

Trumpcard topside - Side A has the ZX polarising slot at top left. Side B is underside.

### Connector Preparation

- 1. Bend connector pins outwards at their midpoint approx. 300
- 2. Bend pins back to centre at their base, such that pinends leave just enough room to accept the Trumpcard PCB.
- 3. Ensuring pin ends are flat against PCB tracks, insert card and align right most pins (adjacent to connector polarising key) with Track 2 Side A.

### Assembly

- 0.6mm single core connecting wire is recommended. Multicore wire is prone to causing shorts after soldering but can be used if necessary. If thin telephone wire is used, all lines can be routed through holes, otherwise construct as per instructions below.
- 1. Slide connector pins approx. 3mm over PCB tracks on the unslotted edge of the card leaving sufficient room to solder connecting wires. Check that the top row of pins is well clear of the bottom row, particularly at their bending point.
- 2. Taking care that the card lies parallel to connector body, tack solder the outermost pins on both sides to the PCB. This facilitates the soldering of the remaining pins, which should be held flush to the PCB tracks for a neater finish.
- 3. Prepare interconnection wires by paring off 3mm insulation at both ends.
- 4. Following the interconnection tables, assemble the underside first, starting at ACE 2B and working thru to 22B. 15B to 19B will need to be threaded under some of the control lines, in order to work through the tracks consecutively. Leave at least 4mm of track free on the ZX side to allow the peripheral to seat properly when applied.
- 5. Turn the assembly over, soldering from ACE 23A to 3A consecutively. Only 10T passes through a hole. Thread the remaining wires between connector body and PCB over its edge to side B.
- N.B. Check your connections and inspect work for solder splashes.

  TAKE PARTICULAR CARE TO CONNECT THE 9V LINE ACE 21A/ZX 22B CORRECTLY.

  TTL chips carry a maximum 7VO rating !!

Having checkedyour wiring thoroughly, attach your peripheral to the card, then holding the card attach it to the ACE's Expansion port. Ensure both peripheral and ACE are on a stable flat surface. N.B. The ACE PCB polarising slot is slightly wider than the connector key itself. With the keyboard facing you and Trumpcard attached, push the connector firmly to the right of the slot - you may hear it click home. Check this point, as a perfectly wired card may not function if connector pins and ACE PCB tracks are slightly misaligned. Screen white-outs or a scrolling cursor indicate a poor or wrong connection.

I/O boards, D/A's, A/D's etc will require homegrown software to test them.

RAMpacks of 16, 32, and 64K will return unsigned RAMTOPS of 32768, 49152 and 65536 (screen display -32768, -16384, Ø respectively) by typing 15384@. which is the ACE's RAMTOP system variable. If the machine crashes, switch off, make necessary adjustments and switch on again. Unless the 9V line is wrongly connected, it is very unlikely that either the ACE or your peripheral is damaged. Check your assembly closely and all should be well.

### ALTERNATIVE APPLICATIONS

Trumpcard need not be used only on the ACE, similarly, the ZX tracks can be used to attach other types of connectors. E.G. Ribbon cable 0.05 and 0.1 pitch/
PCB header pins straight or right-angled on 0.1 centres/wire wrap DIL sockets
0.3" and 0.6". Even 'D' Type subminiature Sockets/Plugs with solder bucket connectors will slip snugly over the PCB edge. The buckets themselves will have to be wired separately - they are not on the same pitch as the PCB tracks.

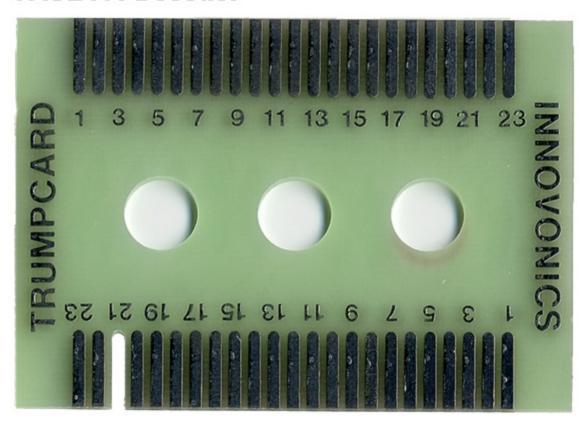
WIRIN	G TABLE	A = Top	side	B = Underside	•				
	Wire Len				ACE	Wire Lengt	h ZX	Route	Signal
3A	2711	7A		<b>D</b> 6	1B			-	
4A	2111	5A		D1	2B	2"	11A		INT
5A	2111	1357	Edge	AØ	3B	2"	12A		NMI
6A	2111	5B	11	A7	4B	2"	13A	H3	HALT
7A	2111	6B	11	A8	5B	2"	14A	19	MREQ
8A	2711	7B	11	A9	6B	2"	15A	11	IORQ
9A	2111	8B	11	A10	7B	Sii	16A	11	RD
10A	211	15B	H2	A2	8B	2"	17A	11	WR
11A	13"	8 <sub>A</sub>		D5	9B	2"	18A	H2	BUSAK
12A	131	9A		D3	10B	2"	19A	11	WAIT
13A	1411	10A		D4	11B	2"	20A	11	BUSRQ
14A	2711	13B	Edge	A15	12B	2"	21A	Ħ	RESET
15A	24" 24" 24" 24" 24"	12B	11	A14	13B	2"	22A	H3	M1
15A 16A	2111	11B	11	A13	14B	2"	23A	11	RFSH
17A	2711	10B	11	A12	15B	. 1골**	4B		<b>A</b> 6
17A 18A 19A	2111	9B	11	19 926 LLV	16B	120	3B		A5
19A	2111	18B	11	ØCIK, E.	17B	171	2B		A4
20A	2111	23B	11	5v * * * * * * * * * * * * * * * * * * *	18B	2"	14B		A3
21A	2411	22B	11	9V	19B	211	16B		A1
22A					20B	2311		H1	DØ
23A	2111	19B+20B	11	OA	21B	اللَّجِ2	6a		D2
LJN	-4	1,2.2.2			22B	2 <del>4</del> 11 2 <del>4</del> 11	1A		D7

Hole 1 is at left when card is viewed from top.

Trumpcard numbering system is independent of any ZX81 convention.



FACE A: Dessus.



FACE B : Dessous.

